

PCI-DIO6400

64 Channel Digital I/O board



INTRODUCTION

This product has performance to be suitable for industry application with high integration digital card. And it supports PCI interface.

GENERAL DESCRIPTION

- ◆ PCI Specification V2.2 32bit 33MHz 5V/3.3V Compatible
- ◆ Non polarity digital input output range.
- ◆ High isolation voltage(5000Vrms)
- ◆ Sink current up to 500mA on each Isolated Output
- ◆ All Inputs are possible interrupt sources
- ◆ Isolated input voltage up to 24V
- ◆ Compact, half-size PCB

APPLICATION

- ◆ Digital Data acquisition
- ◆ Laboratory instrumentation
- ◆ Process control systems

SOFTWARE

- **Operating System**
 - Windows 2000/XP/7/8/10 (32/64bit)
- **Application Programming Interface**
 - Direct control through WDM driver
 - Windows DLL API

SPECIFICATION

■ **Isolated Digital Input**

- Number of Channels : 32
- Number of Common Input : 4
- Maximum Input Range(Non-polarity) : 24V
- Digital Logic Levels :
 - Input High level 5 ~24V
 - Input Low voltage 0 ~ 1.5V
- Input Resistance : 2.4Kohm@0.5W
- Isolation Voltage : 5000Vrms
- Isolated input voltage up to 24V
- Interrupt Sources : All Digital Inputs
- Data Transfer : Programmed I/O

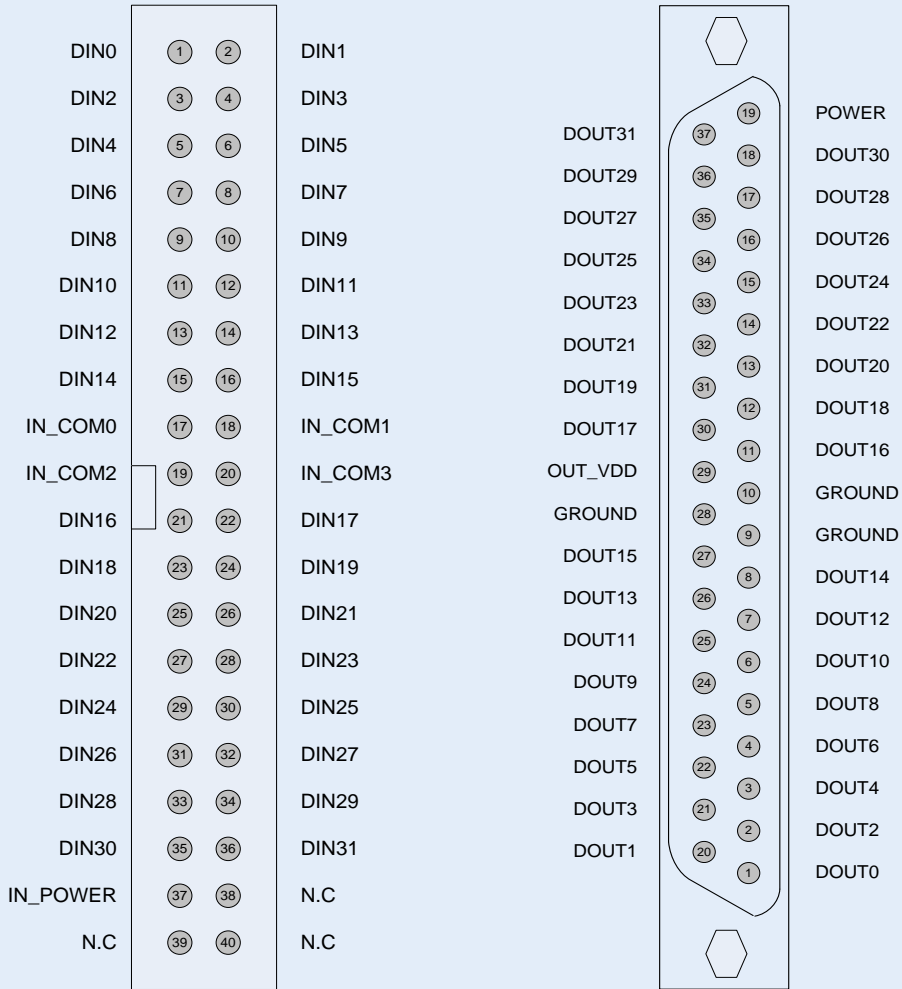
■ **Isolated Digital Output**

- Number of Channels : 32
- Output type : Open collector Darlington transistor
- Sink Current :
 - 500mA for one channel @ 100% duty
 - 500mA for all channel @ 20% duty
- Power dissipation : Max 2.36W per chip (8DO channels)
- Supply Voltage : 5V PCI or Isolated 5V(inside equipped)
- Isolation Voltage : 5000Vrms
- Data Transfer : Programmed I/O

PHYSICAL/ENVIRONMENTAL

Dimension

- Input Connector : Box-shaped Ribbon cable(Hirose HIF3F-40PA-2.54DS)
- Output Connector : Dsub-37pin female Type
- Dimension (not including connectors) : 160mm x 100 mm



40pin Ribbon Cable

Dsub-37pin female

Temperature

- 0 to 70°C, Operating
- -20 to 80°C Storage

Relative Humidity

- 20 to 80 percent, Non-condensing

Power Requirement

- +5V DC(±5%) at Max. 1A

BLOCK DIAGRAM

PCI-DIO6400 INTERNAL BLOCK

PCI BUS

External Interface

