

# **USB-PWM10**

## Pulse width Modulator with USB interface



#### INTRODUCTION

It is the board which was useful composed 4 PWM control block for external interface. You don't need to make separate driver as you recognize it to HID(Hardware Interface Device).

#### GENERAL DESCRIPTION

- ◆ USB2.0 Device Specification
- ◆ High Speed device 480Mbps
- ◆ USB HID Interface
- ◆ 4-Ch Triggered PWM output
- ◆ Digital I/O 4 channels
- ◆ Isolated input voltage up to 24V
- ◆ Compact, half-size PCB

#### **APPLICATION**

- ◆ Laboratory instrumentation
- ◆ Process control systems
- ◆ Factory automation

#### SOFTWARE

#### Operating System

Windows 2000 SP4/XP SP1 over

#### ■ Recommended Software

Visual basic/C++ with Board API(DLL)

#### **SPECIFICATION**

#### **■ PWM output**

• Number of Channels: 4

• Number of Trigger Input: 4

• Resolution: 1us

■ Timer: 22bit

Output Level : 12V(External)

### **■** Isolated Digital Input

• Number of Channels: 6

• Number of Common Input: 1

Maximum Input Range(Non-polarity): 24V

 Digital Logic Levels : input High level 5 ~24V Input Low Voltage 0 ~ 1.5V

• Input Resistance : 4.7Kohm@1.2W

Isolation Voltage: 2500Vrms

Isolated input voltage up to 24V

Data Transfer : Programmed I/O

#### **■** Isolated Digital Output

1

• Number of Channels: 8

Output type : Open collector

■ Isolation Voltage: 5000Vrms

Data Transfer : Programmed I/O

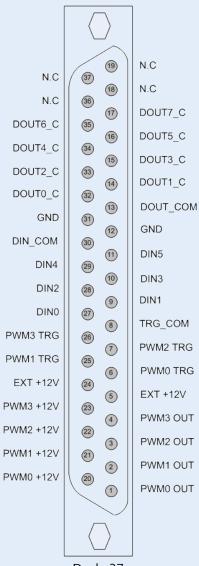


## PHYSICAL/ENVIRONMENTAL

#### Dimension

• Connector : Dsub-37pin

• Dimension (not including connectors): 160mm x 100 mm



Dsub-37

## **■** Temperature

- 0 to 70°C, Operating
- -20 to 80°C Storage

## **■** Relative Humidity

• 20 to 80 percent, Non-condensing

## **■** Power Requirement

■ +5VDC(±5%) at Max. 500mA



## **BLOCK DIAGRAM** USB-PWM10 External Interface USB High Speed Device 2.0 PWM0 Specification PWM1 SIE USB Interface 4K Byte PWM2 USB FIFO FPGA Core Logic PWM3 FPGA Program Digital I/O Logic JTAG