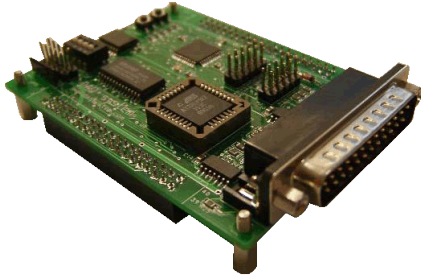


USB-IK01

USB-EK01 I/O Extension Module



INTRODUCTION

It is the board which it was developed so as to be able to supplement USB-EK01(Digital I/O, SRAM, Flash). Specially, It can be implement various H/W interface using EPLD.

GENERAL DESCRIPTION

- ◆ Functional expansion USB-EK01 (Memory, I/O etc.)
- ◆ Xilinx FPGA/EPLD Program Interface(There isn't a separate interface device need.)
- ◆ Exchangeable H/W(memory location) by EPLD Programming
- ◆ I/O extension through EPLD
- ◆ I/O extension through I2C
- ◆ External RAM 32K
- ◆ External ROM(Flash) 64K
- ◆ 8 General I/O(I2C serial interface)
- ◆ Programmable 36 Macro cell

APPLICATION

- ◆ Xilinx FPGA Programmer
- ◆ RAM extension for USB-EK01
- ◆ ROM extension for USB-EK01
- ◆ I/O extension for USB-EK01

SPECIFICATION

Interface

- USB Powered through USB-EK01 Connector
- +3.3V Single Power Operation

Function

- Parallel Interface for Xilinx FPGA/EPLD programming

SOFTWARE

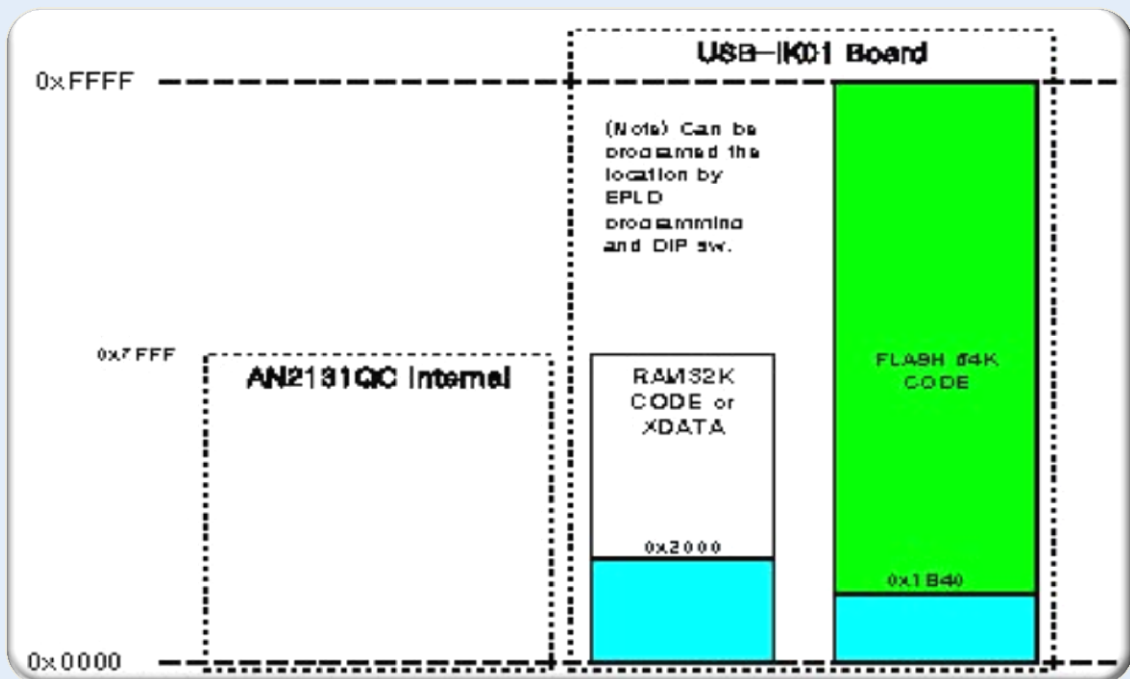
Operating System

- Windows 2000/XP

Recommended Software

- API : Interface with Application through client DLL

MEMORY MAP --- When USB-IK01 put together with USB-EK01



BLOCK DIAGRAM

