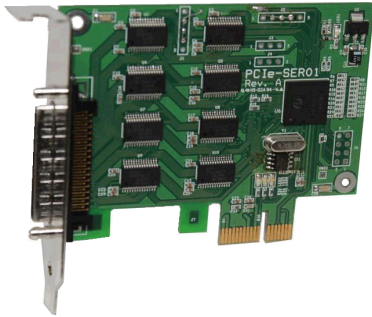


PCIe-SER01

8 Ports RS-232 Terminal Board



INTRODUCTION

PCIe-SER01 is a board having 8 UART (Universal Asynchronous Receiver Transmitter).

GENERAL DESCRIPTION

- ◆ PCI Express 1x Interface
- ◆ PCI Bus Master Operation
- ◆ PCI Express Ver1.1 Compatible
- ◆ Support a PCI Power Management 1.2

APPLICATION

- ◆ Remote Access Server
- ◆ Network/Storage Management
- ◆ Factory Automation and Process Control
- ◆ Multi-port RS232 Cards
- ◆ Point-of-Safe System (PoS)
- ◆ Industrial PC
- ◆ Industrial Control
- ◆ Embedded Systems)

SOFTWARE

- **Operating System**
 - Windows 2000 SP4/XP SP1/7

SPECIFICATION

■ **Features**

- PCI Local Bus Specification V1.0
- PCI Express Ver1.1 Compatible
- PCI Target and Bus Master Operation
- PCI Express 1x Interface

■ **Functions**

- Eight High Performance UART
- Fully 16C550 Software Compatible UARTs
- 128-byte FIFO for each transmitter and receiver
- Data framing size including 5, 6, 7, 8, and 9bits
- Detection of bad data in the receiver FIFO

■ **Temperature**

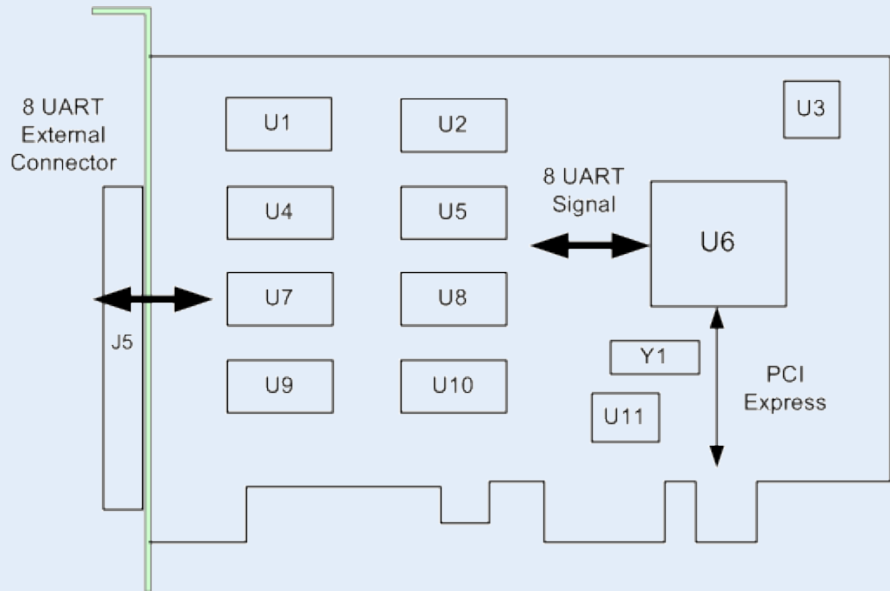
- 0 to 60°C, operating
- -20 to + 80°C storage

■ **Relative Humidity**

- 5 to 95%, Non-considering

Board Feature

J5		
R_DTR7	① ⑤	R_RTS7
R_RI7	② ⑥	R_TX7
R_CTS7	③ ⑦	R_DSR7
R_RX7	④ ⑧	R_DCD7
R_DTR6	⑤ ⑨	R_RTS6
R_RI6	⑥ ⑩	R_TX6
R_CTS6	⑦ ⑪	R_DSR6
R_RX6	⑧ ⑫	R_DCD6
R_DTR5	⑨ ⑬	R_RTS5
R_RI5	⑩ ⑭	R_TX5
R_CTS5	⑪ ⑮	R_DSR5
R_RX5	⑫ ⑯	R_DCD5
R_DTR4	⑬ ⑰	R_RTS4
R_RI4	⑭ ⑱	R_TX4
R_CTS4	⑮ ⑲	R_DSR4
R_RX4	⑯ ⑳	R_DCD4
GND	⑰ ㉑	GND
GND	⑲ ㉓	GND
R_DTR3	⑰ ㉑	R_RTS3
R_RI3	⑱ ㉒	R_TX3
R_CTS3	㉑ ㉔	R_DSR3
R_RX3	㉒ ㉕	R_DCD3
R_DTR2	㉓ ㉖	R_RTS2
R_RI2	㉔ ㉗	R_TX2
R_CTS2	㉕ ㉘	R_DSR2
R_RX2	㉖ ㉙	R_DCD2
R_DTR1	㉗ ㉚	R_RTS1
R_RI1	㉘ ㉛	R_TX1
R_CTS1	㉙ ㉜	R_DSR1
R_RX1	㉚ ㉝	R_DCD1
R_DTR0	㉛ ㉞	R_RTS0
R_RI0	㉜ ㉟	R_TX0
R_CTS0	㉝ ㊱	R_DSR0
R_RX0	㉞ ㊲	R_DCD0



Description of each block of PCIe-SER01

No.	Name	Description
1	U6	Octal UART Interface
2	U1, U2, U4, U5, U7, U8, U9, U10	Port 0 ~ 7
3	J5	68 pin VHDCI(Very High Density Cable Interconnect) connector
4	U11	EEPROM
5	Y1	Oscillator
6	U3	Power Generator

BLOCK DIAGRAM

