

PCI-FRM11

Camera Link Frame Grabber with PCI Interface



INTRODUCTION

The PCI-FRM11 is a PCI board having the function of processing the frame data received from the Camera Link Camera and save the data in the system's main memory. In addition, it has the 8 Digital Inputs and 8 Digital Outputs for external control(option).

GENERAL DESCRIPTION

- ◆ 32bit PCI-BUS 33MHz, Plug & Play
- ◆ PCI Bus Master Operation
- ◆ Supports 1-CH Camera Link Base Configuration
- ◆ Supports Line Scan Camera & Area Scan Camera
- ◆ 8 Channel Digital Input/Output (Photo-coupler – Option)
- ◆ 9600/19200/38400 bps UART Tx/Rx

APPLICATION

- ◆ Black and White, Color Image Display
- ◆ Image Acquisition (Pattern, Particle etc)
- ◆ Inspection Equipment (Sensor, Semiconductor, Device etc)
- ◆ Medical Image Capture (X-ray, Supersonic)

SOFTWARE

■ Operating System

- Windows 2000 SP4/XP SP1/7

■ API

- Interface with Application through client DLL

SPECIFICATION

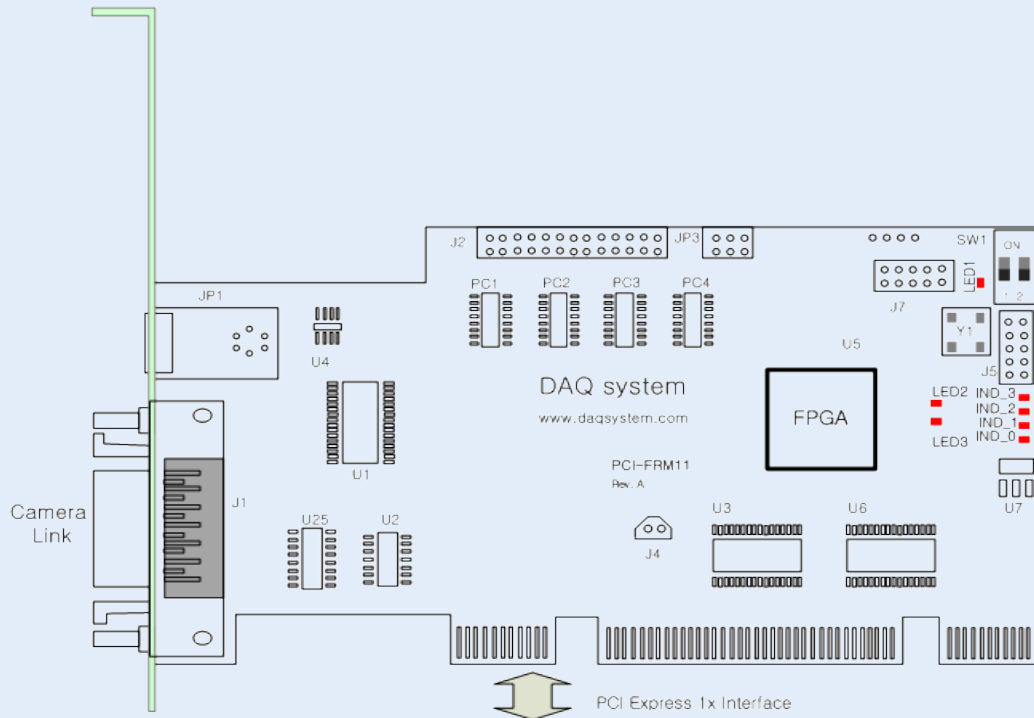
■ Features

- PCI 32 bit 33MHz Interface
- PCI Bus Master Operation
- PCI 5V and 3.3V compatible operation
- 24/16/8bit Image Frame Data Acquisition
- UART communications(8bit Data, 1 Start, 1 Stop, No Parity, 9600/19200/38400bps)

■ Functions

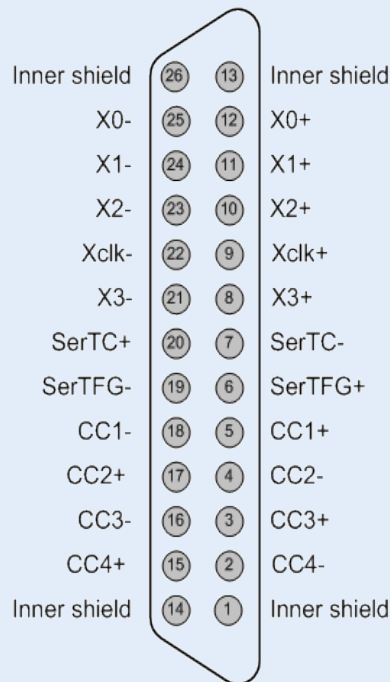
- All of the board functions are controlled by the logic program of the FPGA.
- Receive image frame & UART signal receive/transmit & Digital I/O Output through LVDS interface
- 8bit Digital Input : Photo-coupler input
8bit(Optional)
- 8bit Digital Output :Photo-coupler output
8bit(Optional)

Board Feature



Connector Map

Frame Grabber



MDR 26pin

PHYSICAL/ENVIRONMENTAL

■ From Factor

- PCI

■ Dimension

- Camera Link Connector : MDR 26pin
- Dimension (not including connectors) : 122mm x 64 mm

■ Temperature

- 0 to 60°C, Operating
- -20 to 80°C Storage

■ Relative Humidity

- 5 to 95%, Non-considering

■ Power Requirement

- 3.3V Single Power operation at Max. 300mA

BLOCK DIAGRAM

