

cPCI-DIO02

3U Compact PCI 128 Channel Digital I/O Board



INTRODUCTION

The cPCI-DIO02 is designed for High speed digital data logger with Compact PCI interface. It is composed of 128bit I/O ports for external interface. It can be usable a selection Input/Output at a program. It is possibility selective Read/Write to 16bit each 8 groups.

GENERAL DESCRIPTION

- ◆ 128bit general purpose I/O, Direction control grouped by 16bit.
- ◆ PCI target 32bit 33MHz 5V/3.3V Compatible
- ◆ Full 33Mhz burst read/write operation
- ◆ Average data rate is 30Mb data to, 8Mb data from the board without DMA
- ◆ Very flexible to upgrade because FPGA is used as PCI bridge.

APPLICATION

- ◆ Digital Data Acquisition
- ◆ Laboratory Instrumentation
- ◆ Process Control Systems
- ◆ Factory Automation

SOFTWARE

■ Operating System

- Windows 2000/XP

■ Recommended Software

- Visual basic/C++ with Board API(DLL)

SPECIFICATION

■ Digital Input/Output

- Number of Channels : 128
- Maximum Transfer Speed : 8Mbytes/sec
- Selectable Read/Write to 16bit each 8 groups
- Data Transfer : Programmed I/O
- Compact PCI Target 32Bit/33Mhz-5V/3.3V compatible
- 3U Size Compact PCI

PHYSICAL/ENVIRONMENTAL

■ From Factor

- Compact PCI

■ Dimension

- Front Panel : MDR 100 pin connector
- I/O Connector : Compact PCI female(Emi 064785) Type
- Dimension (not including connectors)
: 160mm x 100 mm

■ Board Feature

■ Temperature

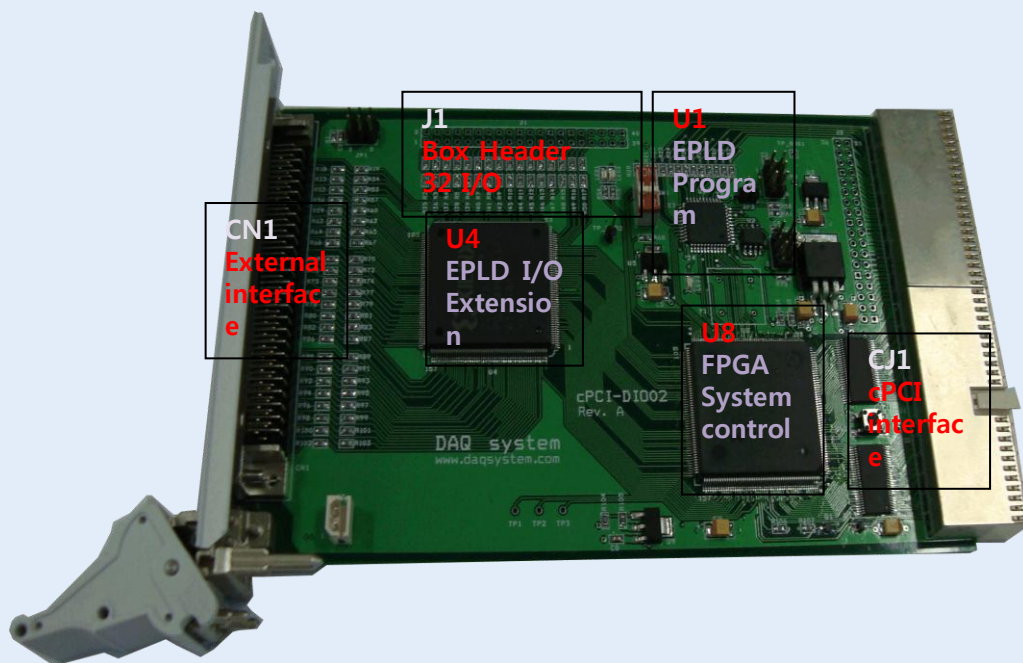
- 0 to 60°C, Operating
- -20 to 80°C Storage

■ Relative Humidity

- 5 to 95%, Non-considering

■ Power Requirement

- +5V DC($\pm 5\%$) at Max. 250mA



- CN1 : MDR 100 pin Connector / External Interface
- J1 : Box Header 2x20 / External Interface
- CJ1 : Compact PCI Interface
- U1 : Serial Flash Program EPLD, U4: I/O Extension EPLD , U8 : System Control FPGA

BLOCK DIAGRAM

